

TI-28444

Patent Amendment

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

B1  
1 (Currently Amended). A method of encrypting a digital signal comprising:  
generating a plurality of pseudo-noise sequences;  
inserting a segment of a first pseudo-noise sequence into a second pseudo-noise sequence, or portion thereof, at an arbitrary position in said second pseudo-noise sequence ~~concatenating said pseudo-noise sequences, or portions thereof,~~ to generate an augmented pseudo-noise sequence; and  
encrypting a data stream using the augmented pseudo-noise sequence.

2 (Currently Amended). The method of claim 1 wherein said generating step comprises the step of generating ~~first and second~~ two pseudo-noise sequences.

3 (Original). The method of claim 1 wherein said generating step comprises the step of generating three or more pseudo-noise sequences.

4 (Canceled)

5 (Currently Amended). The method of claim ~~4~~ 1 wherein said segment has an arbitrary length.

6 (Currently Amended). The method of claim ~~4~~ 1 wherein said segment has arbitrary starting and ending positions within said first pseudo-noise sequence.

7 (Original). The method of claim 1 and further comprising the step of starting the output of the augmented pseudo-noise sequence at an arbitrary position in the sequence.

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8 (Original). The method of claim 1 and further comprising the step of synchronizing the augmented pseudo-noise sequence to a reference clock.

9 (Currently amended). The method of claim ~~8~~ 21 wherein said synchronizing step comprises the step of synchronizing the augmented pseudo-noise sequence to a reference clock relative to an arbitrary offset.

B<sup>1</sup>

10 (Currently amended). Apparatus for encrypting a digital signal comprising:  
two or more pseudo-noise sequence generators  
circuitry for inserting a segment of a first pseudo-noise sequence into a second pseudo-noise sequence, or portion thereof, at an arbitrary position in said second pseudo-noise sequence ~~circuitry for concatenating said pseudo-noise sequences, or portions thereof,~~ to generate an augmented pseudo-noise sequence; and  
an encrypting circuit for correlating the augmented pseudo-noise sequence with a data stream.

11 (Canceled).

12 (Original). The apparatus of claim 10 wherein said two or more pseudo-noise sequence generators comprises three or more pseudo-noise sequence generators.

13 (Canceled).

14 (Currently amended). The apparatus of claim ~~13~~ 10 wherein said segment has an arbitrary length.

15 (Currently amended). The apparatus of claim ~~13~~ 10 wherein said segment has arbitrary starting and ending positions within said first pseudo-noise sequence.

16 (Currently amended). The apparatus of claim ~~13~~ 10 wherein said encrypting circuit performs an exclusive-or operation.

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17 (Original). The apparatus of claim 10 and further comprising circuitry for starting the output of the augmented pseudo-noise sequence at an arbitrary position in the sequence.

18 (Original). The apparatus of claim 10 and further comprising circuitry for synchronizing the augmented pseudo-noise sequence to a reference clock.

B' 19 (Currently amended). The apparatus of claim ~~18~~ 23 wherein said synchronizing circuitry comprises circuitry for synchronizing the augmented pseudo-noise sequence to a reference clock relative to an arbitrary offset.

20 (New). A method of encrypting a digital signal comprising:  
generating a plurality of pseudo-noise sequences;  
concatenating said pseudo-noise sequences, or portions thereof, to generate an augmented pseudo-noise sequence;  
starting the output of the augmented pseudo-noise sequence at an arbitrary position in the sequence; and  
encrypting a data stream using the augmented pseudo-noise sequence.

21 (New). A method of encrypting a digital signal comprising:  
generating a plurality of pseudo-noise sequences;  
concatenating said pseudo-noise sequences, or portions thereof, to generate an augmented pseudo-noise sequence;  
synchronizing the augmented pseudo-noise sequence to a reference clock; and  
encrypting a data stream using the augmented pseudo-noise sequence.

22 (New). Apparatus for encrypting a digital signal comprising:  
two or more pseudo-noise sequence generators  
circuitry for concatenating said pseudo-noise sequences, or portions thereof, to generate an augmented pseudo-noise sequence;

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circuitry for starting the output of the augmented pseudo-noise sequence at an arbitrary position in the sequence; and  
an encrypting circuit for correlating the augmented pseudo-noise sequence with a data stream.

B1  
23 (New). Apparatus for encrypting a digital signal comprising:  
two or more pseudo-noise sequence generators  
circuitry for concatenating said pseudo-noise sequences, or portions thereof, to generate an augmented pseudo-noise sequence;  
circuitry for synchronizing the augmented pseudo-noise sequence to a reference clock; and  
an encrypting circuit for correlating the augmented pseudo-noise sequence with a data stream.

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